



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Levine et al.**

Serial No.: **10/675,783**

Filed: **September 30, 2003**

For: **Method and Apparatus to  
Autonomically Profile Applications**

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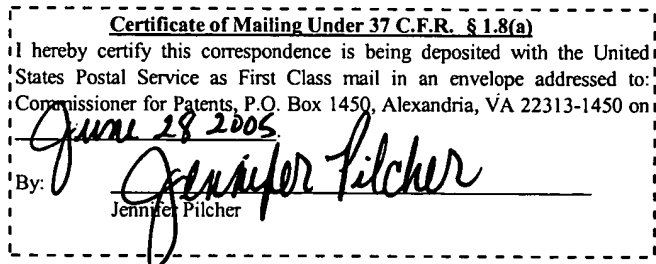
Group Art Unit: **2182**

Examiner: **Unknown**

Attorney Docket No.: **AUS920030486US1**

**35525**

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER



**TRANSMITTAL DOCUMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

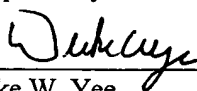
Sir:

ENCLOSED HERewith:

- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- References AA-AJ; and
- Our return postcard.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,



Duke W. Yee

Registration No. 34,285

**YEE & ASSOCIATES, P.C.**

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ATTORNEY FOR APPLICANTS



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§ Examiner: **Unknown**

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 09-0447.

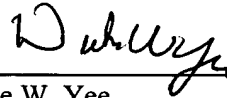
In accordance with 37 C.F.R. 1.97(h), the filing of this Supplemental Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Supplemental

Information Disclosure Statement shall not be construed to mean that a search has been made.

This Supplemental Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee is required.

Date: 6/21/05

Respectfully submitted,

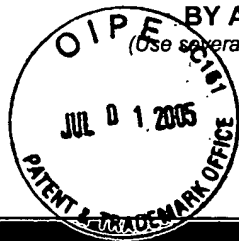


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Form PTO-1449

LIST OF PRIOR ART CITED  
BY APPLICANT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.  
**AUS920030486US1**SERIAL NO.  
**10/675,783**APPLICANT **Levine et al.**FILING DATE **September 30, 2003**GROUP ART UNIT **2182**

## RELATED PATENT APPLICATIONS

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
	AF	10/675,751 / AUS920030482US1	Levine et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
	AI	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Exception on Specified Instructions	Sep. 30, 2003
	AJ	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.